

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Abhishek Lal
Application No. : 10/615,601
Confirmation No. : 8291
Filed : July 7, 2003
For : BINARY DECODERS IN ELECTRONIC INTEGRATED
CIRCUITS

Examiner : Jean Bruner Jeanglaude
Art Unit : 2819
Docket No. : 852463.402
Date : April 23, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FEE DEFICIENCY AUTHORIZATION FORM

Commissioner for Patents:

Applicant hereby authorizes the Director to charge any deficiencies in fees due by way of the enclosed papers only under 37 CFR 1.16 and 1.17 to Deposit Account No. 19-1090.

Respectfully submitted,

SEED Intellectual Property Law Group PLLC

/E. Russell Tarleton/

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